81754.0118 Express Mail Label No. EV 324 112 455 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re application of: Terunao HANAOKA of Nagano-ken, Japan

Serial No: Not assigned Filed:

March 17, 2004

For:

Semiconductor Device and Method for Manufacturing the Same, Circuit **Board and Electronic Equipment**

Mail Stop PATENT APPLICATION

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

CORRESPONDENCE ADDRESS:

000026021

Transmitted herewith for filing is the patent application identified above. 2 sheet(s) of drawings (formal informal) is(are) enclosed. 15 page(s) of specification and _1 page(s) of abstract of the invention are enclosed. An assignment of the invention to SEIKO EPSON CORPORATION is enclosed will follow. An associate power of attorney is enclosed will follow. Applicant claims small entity status. See 37 CFR 1.27. Declaration and Power of Attorney is enclosed will follow. A certified copy of Japanese Patent Application No. 2003-073925 filed March 18, 2003 from which priority is claimed under 35 U.S.C. § 119 will follow. IDS enclosed (with _2 reference(s)). Preliminary Amendment is enclosed. Application data sheet is enclosed. See 37 CFR 1.76. Return postcard is enclosed.										
		•	LCULA	TION OF FEES			,		_	
	ITEM	TOTAL NO. OF CLAIMS		NO. OF CLAIMS OVER BASE	LG/S \$ ENTIT		\$ AMOUNT	\$ FEE		
A	TOTAL CLAIMS FEDE	20	-20	0	LG=\$18 SM=\$9	\$18	\$ 0			
В	INDEPENDENT CLAIMS FEE*	3	-3	0	LG=\$86 SM=\$43	\$86	\$ 0			
С	C SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B)							0	┙	
D	D MULTIPLE-DEPENDENT CLAIMS FEE LARGE ENTITY FEE = \$290 SMALL ENTITY FEE = \$145						\$ - 0			
E	E BASIC FEE LARGE ENTITY FEE = \$770 SMALL ENTITY FEE = \$385							\$ 770		
F	F TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E)							\$ 770		
G	G ASSIGNMENT RECORDING FEE \$40							\$ 40	_	
	*LIST INDEPENDENT CLAIMS 1, 7 and 11.									
	 A check in the amount of \$_0 to cover the filing fee is enclosed. A check in the amount of \$_40.00 to cover Assignment Recordation fee is enclosed. The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed. Any additional filing fees required under 37 C.F.R. 1.16 Any patent application processing fees under 37 C.F.R. 1.17 									
500 S	March 17, 2004 South Grand Avenue, Suite 19 Ingeles, CA 90071	Respectfyll sub HOGAN & HAR By:	rie No 41,2							

\\LA - 81754/0118 - 197579 v1

Telephone:

Facsimile:

(213) 337-6700

(213) 337-6701

PATENT

Attorney Docket No: 81754.0118

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Art Unit: Not assigned						
Terunao HANAOKA	Examiner: Not assigned						
Serial No: Not assigned							
Filed: March 17, 2004							
For: Semiconductor Device and Method for Manufications Same, Circuit Board and Electronic Equipment							
CERTIFICATE OF MAILING "Express Mail" Mailing Labe Date of Deposit:	No. <u>EV 324 112 455 US</u>						
Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450							
Dear Sir:							
I hereby certify that							
Mail Stop PATENT APPLICA Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450							
Date: March 17, 2004	Reynaldo Gallardo						
Hogan & Hartson, LLP 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071 Telephone: 213-337-6700 Facsimile: 213-337-6701	Name of person mailing papers Signature						